# Approximation Algorithms for Dependency-Aware Rule-Caching in Software-Defined Networks 

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## 1. Introduction of Rule Caching

- Rule caching

Install packet-processing rules in switches

- Switch types

| Switch types | Pros | Cons |
| :--- | :--- | :--- |
| Hardware | Fast <br> $(>400$ Gbps $)$ | Small in capacity <br> $(2 \mathrm{~K} \sim 10 \mathrm{~K})$ |
| Software | Large in capacity | Slow (40 Gbps) |

- Hardware: Ternary Content Addressable Memory (TCAM)
- Software: Software-based switches



## General Rule Matching Problem

Rule Table

| Rule | Code | Priority | Weight |
| :---: | :---: | :---: | :---: |
| $R_{1}$ | 000 | 6 | 10 |
| $R_{2}$ | $00^{\star}$ | 5 | 60 |
| $R_{3}$ | $0^{\star \star}$ | 4 | 30 |
| $R_{4}$ | $11^{\star}$ | 3 | 5 |
| $R_{5}$ | $1^{\star} 0$ | 2 | 10 |
| $R_{6}$ | $10^{\star}$ | 1 | 120 |



- Rule dependency graph
- Use of wild card * to reduce rule number
- Directed acyclic graph: rule and all its decedents (to be in cache) Maximum traffic-hit by placing no more than k rules
- NP-hard ${ }^{[1]}$
[1] Cacheflow: Dependency-aware rule-caching for software-gdefined networks (SOSR'16)


## Efficient Rule Caching

## Assumption

- Prefix coding to reduce rule number (optimal coding ${ }^{[2]}$ is hard)

TCAM forwarding table

- All rules form a forest of trees
- Constraint
- Descendant constraint
- Limited number of cached rules $k$
- Objective
- Maximize number of rules hits



## A Motivating Example



With maximum hit

## 2. Solutions

## Greedy Solution One (Branch)

 Definition- Branch (which includes fork)
- A rule and all its descendants
- Max branch


Branch in a max branch

- If it meets either of the two conditions:
(1) Branch size is $k$
(2) If size < $k$, not a branch of another branch with a size of $k$ or less
- Maintaining max branches will include all cacheable branches

| Definition | Explanation |
| :--- | :--- |
| Unit cost $C$ | Each rule has a unit cost |
| Weight $W$ | Rule hits |
| Unit benefit $\Delta W / \Delta C$ | Ratio of rule weight to rule cost |

## Greedy Solution One

## Steps

- Select the branch with the maximum unit benefit ( $\Delta W / \Delta C$ )
- Update unit benefit values of other branches
- Use a heap to maintain max unit benefit for each max branch
- Time complexity
$O\left(n+k \log n+k^{2}\right)$
- n : rule number
- k: cache size
- Approximation ratio: 2

Optrimal unit benefit
$(43+13+7+12=26) / 5=19$
Optrimal unit benefit
$(43+13+7+12=26) / 5=19$


## 2. Solutions (cont'd)

Greedy Solution Two (Segment)

- Definition
- Segment
- Cut off a branch
- Deny rule
- A dummy rule to forward to the software switch
- Cut branches with low-weights
- Unit benefit ( $\Delta W / \Delta C+1$ )
- We only consider segments without a fork
- To avoid non-polynomial number of choices


Segment in a max segment

(a) A fork

(b) Three segments Converting a folk into multiple segments

## Greedy Solution Two

## Steps

Select the max segment with the maximum unit benefit

- Update unit benefit values of other segments
- Use two heaps to maintain segments

Time complexity:


Constructing the global heap
( $g$-heap) from the max heaps
Constructing the global heap
( $g$-heap) from the max heaps of local heaps (I-heaps)

Optimal unit benefit withadendendersules $430(T+1)=21135$

- $O(\mathrm{kn})$



## 2. Solutions (cont'd)

## Combined Greedy Solution

## Insight

Combine the two greedy solutions

- Use branch and segments with the same criterion
- Maximum unit benefit
- Each maintains its own heap
Time complexity
- O(kn)
- Approximation ratio
- 24/5


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k=3
$$

Optimal unit benefit with deny rules
$(43+20) /(2+1)=21$

## 2. Solutions (cont'd)

## Dynamic Programming (DP) Solution



## Dynamic Programming Solution (cont'd)

## $T[R, d]$

- Subtree of rule R, and its first $d$ children's subtrees
- Depth-first-search
$O[R, d, m]$
- Optimal cache-hits by caching m rules in $T[R, d]$
- $O\left[R_{0}, d\left(R_{0}\right), k\right]$

Our objective

- $R_{0}$ t tree root
- $d\left(R_{0}\right)$ : all Ro's children


## Initialization

$O\left[R_{i}, 0, m\right]=\left\{\begin{array}{cc}W_{i} & \text { if } m \geq 1 \text { and } i \neq 0 \\ 0 & \text { otherwise }\end{array}\right.$

## Formulation

$O\left[R_{i}, d, m\right]=\max \left\{O\left[R_{i}, d-1, m\right]\right.$,
$\left.\max _{0 \leq m^{\prime} \leq m}\left[O\left[R_{i d}, d\left(R_{i d}\right), m^{\prime}\right]+O\left[R_{i}, d-1, m-m^{\prime}\right]\right]\right\}$

- $R_{i d}$ : d-th child of $R_{i}$


## 7. Simulation

- Comparison algorithms ${ }^{[1]}$
- Dependent
- Branch without using heap
Cover
- Segment without using heaps

Our algorithms

- Branch
- Segment

- Combined
- DP (optimal)


## Settings

## Data sets

- CAIDA packet trace
- 12,000 forwarding rules
- Stanford Backbone packet trace
- 180,000 forwarding rules Stanford University IT
- Metrics
- Execution time
- Cache-hit ratio with TCAM size
- Cache-hit ratio with number of packets
- Variables

TCAM cache size: $k=63 \sim 2000$

## Simulation Results


(a) Algorithm execution time.

(b) Cache hit traffic and TCAM size.

(c) Cache hit traffic and the number of packets.

CAIDA packet trace

- DP has a much larger execution time than others
- Branch is faster than Dependent because of using heaps
- Our four algorithms achieve at least a $79.8 \%$ hit ratio with 2,000 cache size, which is just $1.1 \%$ of the total rule table.
- DP achieves the best cache-hit ratio.


## Simulation Results (cont'd)


(a) Algorithm execution time.

(b) Cache hit traffic and TCAM size.

(c) Cache hit traffic and the number of packets.

Stanford backbone trace

- More rules result in a much larger execution time
- Our three greedy algorithms achieve better ratios than CAIDA one with the same TCAM size because of deeper dependencies
- For 30 million packets, DP's cache-hit ratio reaches 90.2\%, Combined reaches $89.4 \%$, Segment reaches $83.7 \%$ and Branch reaches $81.9 \%$ with 2,000 cache size.


## 8. Conclusion

- Hardware and software switches
- Caching technology
- Wildcard (*) rule matching
- Rule dependency constraints
- Deny rule
- limited number of rules in TCAM
- Objective
- Maximize cache-hit ratio
- Solutions
- Three greedy algorithms with approximation ratios
- Optimal DP solution

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Q \& A
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